

U.S. UTILITY Patent Application

**PATENT NUMBER and
ISSUE DATE**

APPL NUM 10058290	FILING DATE 01/30/2002	CLASS 438	SUBCLASS	GAU 2812	EXAMINER Pere
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****APPLICANTS:** Okase Wataru; Matsuo Takenobu

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN P2001-21619 01/30/2001

PG-PUB	DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no
Verified and Acknowledged Examiner's initials		
		ATTORNEY DOCKET NO P 290584 FEL0114-USA
TITLE : Plating apparatus and method of manufacturing semiconductor device		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims		Print Claim for O.G
ISSUE FEE		Primary Examiner	DRAWING		
Amount Due	Date Paid		Sheets Drwg.	Figs.Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

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